Amendments to the Claims:

Please amend the claims as follows:

1. (Currently amended) A complementary signal generator for outputting complementary positive-phase and antiphase signals that vary between a first logical value and a second logical value, comprising:

a signal forming unit for outputting a positive-phase intermediate signal being in phase with an input signal varying between the first logical value and the second logical value to a positive-phase intermediate node, and an antiphase intermediate signal antiphase to the input signal to an antiphase intermediate node;

a positive-phase signal output part;

an antiphase signal output part; ~~and~~

a first connecting means connected among the positive-phase intermediate node, the antiphase intermediate note, the positive-phase signal output part, and the antiphase signal output part and having a first control terminal for receiving a first control signal produced in synchronism with a state change of the input signal from the first logical value to the second logical value, and for simultaneously transferring the second logical value of the positive-phase intermediate signal and the first logical value of the antiphase intermediate signal to the positive-phase signal output part and the antiphase signal output part respectively; and

a driving means for individually setting respective states of the positive-phase signal output part and the antiphase signal output part to the first logical value and the second logical value.

2. (cancelled)

3. (Previously presented) The complementary signal generator according to claim 1, further comprising a second connecting means connected among the positive-phase intermediate node, the antiphase intermediate node, the positive-phase signal output part, and the antiphase signal output part and having a second control terminal for receiving second control signal

produced in synchronism with a stage change of the input signal from the first logical value to the second logical value, and for simultaneously transferring the first logical value of the positive-phase intermediate signal and the second logical value of the antiphase intermediate signal to the positive-phase signal output part and the antiphase signal output part, respectively.

4.(Currently amended) The complementary signal generator according to claim 1, wherein the first logical value corresponds to an "L" level, and the second logical value corresponds to an "H" level.

5. (Previously presented) The complementary signal generator according to claim 3, wherein each of the first and second connecting means has analog switches that comprise a pair of parallel-connected P channel and N channel type FETs.

6. (Previously presented) The complementary signal generator according to claim 1, wherein the first connecting means inhibits the transfer of the logical values of the positive-phase intermediate node and the antiphase intermediate node.

7. (Previously presented) The complementary signal generator according to claim 3, wherein the second connecting means inhibits the transfer of the logical values of the positive-phase intermediate node and the antiphase intermediate node.